

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul Elliott
Application No. : 10/658,595
Filed : September 9, 2003
For : BRIDGE CIRCUIT FOR USE IN RETIMING IN A
SEMICONDUCTOR INTEGRATED CIRCUIT

Examiner : Nimesh G. Patel
Art Unit : 2111
Docket No. : 851963.413
Date : April 22, 2008

Mail Stop RCE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

Commissioner for Patents:

In furtherance to the Notice of Appeal mailed on January 22, 2008 which was in response to the final Office Action mailed August 21, 2007, please extend the period of time for response one month, to expire on April 22, 2008. Attached are an Electronic Fee Transmittal and the requisite extension fee. Please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks begin on page 8 of this paper.